



QIS INSTITUTE OF TECHNOLOGY :: ONGOLE

(Approved by AICTE, New Delhi & Affiliated to JNTUK, Kakinada)

(An ISO 9001:2015 Certified Institution)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Curriculum Gaps and Content Delivery

List of the curriculum gaps for the attainment of defined POs and PSOs.

List of identified curricular gaps in R13 Regulation:

Table Curriculum Gaps

subject	Topics
EMWTL	Review of Co-ordinate Systems,
PDC	AOI Logic,
DSD- DICA	VHDL Programming using structural and data flow modeling, Basic design steps
MPMC	8051 Interfacing Intel 8237a DMA CONTROLLER
VLSI	Fault types and Models, Controllability and Observability Introduction to Low Power VLSI Design

Curriculum gaps identified based on PO correlation with all subjects as per program specific curriculum

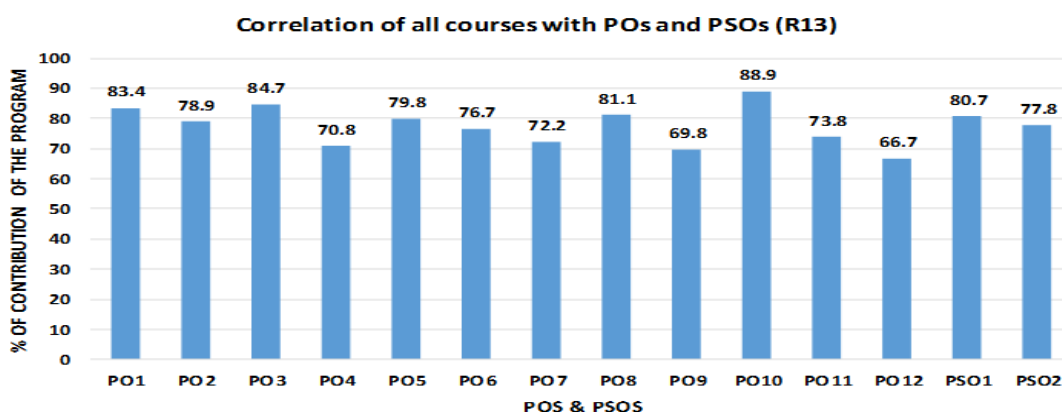


Fig: program level courses contribution to POs

The above figure represents the program level courses contribution to POs and PSOs. It is seen that percentage of contribution to all POs are different. By keeping 80% as the threshold value, the following curriculum gaps are identified in the course structure of R13.

- PO2 has variation of 1% from the threshold value. So curriculum needs more focus on Problem Analysis aspect.
- PO4 has variation of 9% from the threshold value. So curriculum needs focus more on research based knowledge.
- PO6 has variation of 3% from the threshold value because Curriculum is offering only few courses towards the social responsibility for an engineering graduate and ethics to be inculcated. So curriculum needs more focus on social responsibility.
- Curriculum is less focusing on Environment and sustainability, so graduates need more awareness on Environment and sustainability.
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Steps taken to get identified gaps included in the curriculum (2)

Based on the gaps on the identified in the curriculum, a letter had been sent to the JNT University, Kakinada to incorporate the gaps in the curriculum in the next regulations. Department Academic Committee shall discuss about the content of the current scheme with the help of course feedback surveys, graduate surveys, alumni surveys, employer surveys etc. and shall formulate recommendations for the next scheme.

Delivery details of content beyond syllabus

- Assignments with reference to Internet resources and NPTEL video lectures.
- Additional laboratory experiments.
- Campus Recruitment Training.
- Soft Skill Training
- Guest lecturers, Workshops, Seminar and Conferences
- Industrial Visit and Internship

2017-2018

Table: List of Events (content beyond the syllabus)

S. No.	Gap	Action taken	Date-Month-Year	Resource Person with designation	% of Students	Relevance to POs, PSOs
1	Field Programmable Gate Array implementation using verilog	Workshop has been arranged	15-02-18	Mr.SK.CHAND BASHA, Design Verification engineer, SION semiconductor Pvt. Ltd, Bangalore	95	PO3, PO4, PO9, PS02
2	Internet of Things with emphasis on sensors & interfacing	Workshop has been arranged	25-09-17 to 26-09-17	Mr.K.S.RATHINA SWAMY, Technical Manager, ENTHU TECHNOLOGY SOLUTIONS INDIA PVT.LTD, Coimbatore	90	PO3, PO4, PO9, PS02
3	Virtualization in Cloud Computing	Guest lecture has been arranged	08-09-17	Dr.R.A.KARTHIKA, Assistant Professor, VELS University, Chennai	91	PO3, PO4, PO9, PS02
4	Control systems	Guest lecture has been arranged	08-03-18	Dr.A.MURALI VARA PRASAD, Professor, SACET, Chirala	97	PO3, PO4, PO9, PS02
5	Analysis of Bistable Multivibrator	Guest lecture has been arranged	09-02-18	Dr.Ch. Hima Bindu, Professor, ECE, QISCET, Ongole	96	PO3 PO4, PO9, PS02
6	MRI Scan	Guest lecture has been arranged	16-02-18	Dr.Ch. Hima Bindu, Professor, ECE, QISCET, Ongole	98	PO3, PO4, PO9, PS02

organized:

2016-2017

S.No.	Gap	Action taken	Date- Month- Year	Resource Person with designation	% of Students	Relevance to POs, PSOs
1	Software Testing Tools	3 Day workshop has been arranged	15-9-2016 to 17-9-2016	MR. ABDUL AZEEZ, TEST MANAGER, CAPGEMINI, HYDERABAD.	95	PO2,PO5, PO9&PSO2
2	Cloud Computing	Guest lecture has been arranged	4-10-2016	DR.R.A. KARTHIKA,ASST.P ROFESSOR, VELS UNIVERSITY.	94	PO2,PO5, PO9& PSO2
3	Internet Of Things	Guest lecture has been arranged	12-1-2017	DR.R.A.KARTHIKA, ASST.PROFESSOR, VELS UNIVERSITY.	91	PO2, PO5, PO9& PSO2

2015-2016

S.No.	Gap	Action taken	Date- Month- Year	Resource Person with designation	% of students	Relevance to POs, PSOs
1	PCB Design	A Two-day Workshop has been arranged	14-9-2015 to 15-9-2015	Mr.V.GOVINDA RAO, Microlink Peripheral control Pvt. Ltd, Vijayawada	95	PO2,PO5,PO9 & PSO2
2	Methodologies On Embedded system Design	Workshop has been arranged	17-8-2015	Mr.V.GOVINDA RAO, Microlink Peripheral control Pvt. Ltd, Vijayawada	85	PO2,PO5,PO9 & PSO2
3	Analog Communication	Guest lecture has been arranged	27-2-2016	SIR.N.JANARDHAN, Gernal manager, BSNL, Bangalore	90	PO2,PO5,PO9 &PSO2
4	VLSI Design	Guest lecture has been arranged	26-9-2015	Dr. G. Vairavel, Professor and Head, ECE Department, Vel tech. University,	96	PO3,PO4, PO9,PO10 &PSO1

				Chennai		
5	Awareness on Management Studies	Seminar has been arranged	14-10-2015	Mr.G.GOPI, Sr. Academic counselor, BMSR Organization	94	PO2,PO5,PO9 & PSO2
6	Antenna and Wave Propagation	Seminar has been arranged	22-8-2015	Dr. G. Vairavel, Professor and Head, ECE Department, Vel tech. University, Chennai	95	PO3,PO4, PO9,PO10 &PSO1
7	Radar System	Seminar has been arranged	19-8-2015	Dr K Veerasamy, Principal, QISCET, Ongole	92	PO2,PO5,PO9 & PSO2
8	Seminar On GATE	Seminar has been arranged	12-8-2015	BANDARI. SANDEEP,DEPUTY General Manager T.I.M.E, Institute, Ongole.	93	PO3,PO4, PO9,PO10 &PSO1

Mapping of content beyond Syllabus with the PO's & PSO's (3)

Table : Mapping of content beyond Syllabus with the PO's & PSO's

PO'S	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12	PSO 1	PSO 2
Topics														
Pre-placement Training	✓	✓	✓			✓	✓			✓	✓	✓	✓	✓
Training on Soft skills		✓	✓	✓	✓	✓					✓	✓		✓
Innovative Projects	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓		✓	✓	✓
Guest lectures	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
workshops	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓		✓
Industrial Visits		✓	✓	✓		✓	✓			✓	✓	✓		✓